

ABSTRACT OF THE DISCLOSURE

An inter-processor communications interface is disclosed. The interface permits the transparent movement of data from one processor to another via a memory fabric which is connected with both processors. This permits the incoming data of a first processor to be utilized by a second processor thereby freeing that processor from having to handle incoming data. Further, the second processor can handle outgoing data exclusively, freeing the first processor from having to handle outgoing data. In this way, each direction of a bi-directional dataflow may be handled by the maximum capability of a bi-directional capable processing device.